Cambridge Assessment International Education

CS BY AQIB KHAN

# Cambridge IGCSE™

**COMPUTER SCIENCE** **0478/11**

Paper 1 **May/June 2021**

MARK SCHEME

Maximum Mark: 75

**Published**

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the May/June 2021 series for most Cambridge IGCSE™, Cambridge International A and AS Level components and some Cambridge O Level components.

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## Generic Marking Principles

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently, e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

GENERIC MARKING PRINCIPLE 5:

Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).

GENERIC MARKING PRINCIPLE 6:

Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

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**Please note the following further points:**

The words in **bold** in the mark scheme are important text that needs to be present, or some notion of it needs to be present. It does not have to be the exact word, but something close to the meaning.

If a word is underlined, this **exact** word must be present.

A single forward slash means this is an alternative word. A double forward slash means that this is an alternative mark point.

Ellipsis (…) on the end of one-mark point and the start of the next means that the candidate **cannot** get the second mark point without being awarded the first one. If a MP has ellipsis at the beginning, but there is no ellipsis on the MP before it, then this is just a follow-on sentence and **can** be awarded **without** the previous mark point.

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| Question | Answer | Marks |
|---|---|---|
| 1(a) | One mark per each correct register.<br><br>1 0 1 0 0 1 1 1<br><br>1 1 0 1 0 1 1 0 | 2 |
| 1(b)(i) | Any **one** from:<br>– Both addresses can be used to identify a computer/device<br>– Both are unique<br>– Both can be represented as hexadecimal<br>– Both addresses do not change if IP address is static | 1 |
| 1(b)(ii) | Any **two** from:<br>– An IP address is assigned by the network/router/ISP, A MAC address is assigned by the manufacturer<br>– An IP address can be changed (if dynamic), MAC address cannot be changed<br>– IP address has 4/8 groups of values, MAC address has 6 groups/pairs of values<br>– IP address is 32-bit/128-bit, MAC address is 48-bit<br>– IP address does not contain serial number/manufacturer number, MAC address does<br>– IP(v4) address is denary and MAC address is hexadecimal | 2 |

| Question | Answer | Marks |
|---|---|---|
| 2(a) | Any **three** from:<br>– Keyboard<br>– Mouse<br>– Microphone<br>– Keypad<br>– Touchscreen<br>– Touchpad | 3 |

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<table>
<tr><th>Question</th><th>Answer</th><th>Marks</th></tr>
<tr><td>2(b)</td><td>One mark for each correct row.
<table>
<tr><th>Statement</th><th>HDD (✓)</th><th>SSD (✓)</th><th>USB flash memory drive (✓)</th></tr>
<tr><td>it has no moving parts</td><td></td><td>✓</td><td>✓</td></tr>
<tr><td>it is non-volatile</td><td>✓</td><td>✓</td><td>✓</td></tr>
<tr><td>it can use NAND gates to store data</td><td></td><td>✓</td><td>✓</td></tr>
<tr><td>it uses magnetic properties to store data</td><td>✓</td><td></td><td></td></tr>
<tr><td>it has the smallest physical size</td><td></td><td></td><td>✓</td></tr>
<tr><td>it has the slowest read/write speeds</td><td>✓</td><td></td><td></td></tr>
</table></td><td>6</td></tr>
<tr><td>2(c)(i)</td><td>Any two from:<br>– It cannot be inserted incorrectly<br>– Supports different transmission speeds<br>– High speed transmission<br>– Automatically detected (not connected) // automatically downloads drivers<br>– Powers the device (for data transfer)<br>– Backward compatible</td><td>2</td></tr>
<tr><td>2(c)(ii)</td><td>– Serial</td><td>1</td></tr>
</table>

| Question | Answer | Marks |
|---|---|---|
| 3(a) | One mark per each correct term in the correct order.<br>– Software<br>– Network<br>– Criteria<br>– Accept // reject<br>– Reject // accept<br>– Hacking | 6 |



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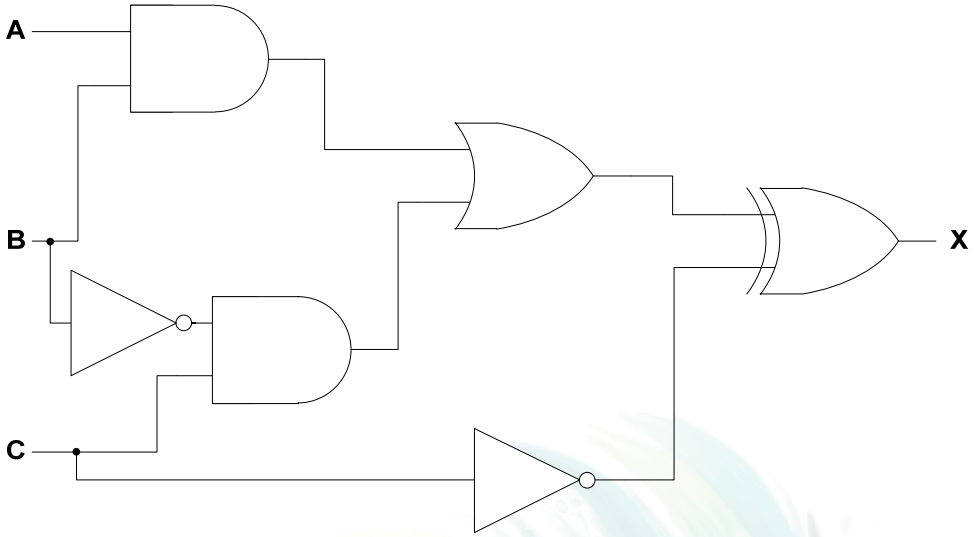
| Question | Answer | Marks |
|---|---|---|
| 3(b) | Any **three** from:<br>– Password<br>– Biometrics (device)<br>– Encryption<br>– Physical methods (e.g. locks)<br>– Two-factor authentication // Two-step verification<br>– Anti-viruses | **3** |

| Question | Answer | Marks |
|---|---|---|
| 4 | Any **six** from:<br><br>Phishing<br>– Legitimate looking email sent to user<br>– encourages user to **click a link** that directs user to a fake website<br>– User encouraged to enter personal details into a fake website // designed to obtain personal details from a user<br><br>Pharming<br>– Malicious code/malware is downloaded/installed // software downloaded without users' knowledge<br>– … that **re-directs** user to fake website (when legitimate URL entered)<br>– User encouraged to enter personal details into a fake website // designed to obtain personal details from a user | **6** |

| Question | Answer | Marks |
|---|---|---|
| 5 | **Eight** from:<br>– Sensor send data/readings/signal to microprocessor<br>– Data is converted from analogue to digital (using ADC)<br>– Microprocessor compares/checks data to stored values/range of values …<br>– … If data is greater than 30 / above the range microprocessor sends **signal** to open window and to turn heater off<br>– … If data is below 25 the microprocessor sends **signal** to turn on heater and to close window<br>– … If data is between 25 and 30 / within the range no action taken<br>– Actuator is used to operate heater/window<br>– **Whole** process is continuous | **8** |

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| Question | Answer | Marks |
|---|---|---|
| 6(a) | One mark per each correct logic gate, with correct input:<br><br> | 6 |
| 6(b) | – Row 1<br>– Row 3<br>– Row 4<br>– Row 5 | 4 |

| Question | Answer | Marks |
|---|---|---|
| 7(a)(i) | **Four** from:<br>– (Compression) **algorithm** is used<br>– No data will be removed // original file can be restored<br>– Example of type of algorithm that would be used e.g. RLE<br>– Repeated patterns in the music are identified<br>– … and indexed<br><br>NOTE: If another lossless method is described, marks can be awarded. | 4 |
| 7(a)(ii) | Any **one** from:<br>– To provide the highest quality of music file (that compression will allow)<br>– The user is able to listen to the original sound file<br>– No loss of quality for the sound file provided | 1 |
| 7(a)(iii) | Any **one** from:<br>– Allow for quicker streaming speed<br>– Would not require as much bandwidth (to stream)<br>– Does not need as much RAM<br>– Smoother listening experience // less lag<br>– Will not use as much of data allowance | 1 |

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| Question | Answer | Marks |
|---|---|---|
| 7(a)(iv) | **Two** from:<br>– Streaming speed may be slower<br>– … and may affect listening experience // buffering may occur<br>– User may need more bandwidth to stream<br>– … that could be more expensive<br>– It would be a larger file size<br>– … so may take longer to upload<br>– … so will take up more storage space …<br>– … on webserver | 2 |
| 7(b) | Any **four** from:<br>– Browser sends URL to DNS<br>– … using HTTP/HTTPS<br>– IP address is found on DNS<br>– DNS returns IP address to the browser<br>– Browser sends request to web server/IP address<br>– Web server sends web pages back to browser<br>– Browser **interprets/renders** the HTML (to display web pages)<br>– Security certificates exchanged | 4 |
| 7(c) | **Two** from:<br>– **Web server** has been flooded with traffic // **web server** has been sent many requests at once<br>– … so, server is brought to a halt / crashes | 2 |

| Question | Answer | Marks |
|---|---|---|
| 8(a) | – Odd<br>– Odd<br>– Even<br>– Even | 4 |
| 8(b) | Any **one** from:<br>– there is a transposition of bits<br>– it does not check the order of the bits (just the sum of 1s/0s)<br>– even number of bits change<br>– incorrect bits still add up to correct parity | 1 |
| 8(c)(i) | **Four** from:<br>– Multiple **bits** are sent **at the same time**<br>– Uses multiple wires<br>– Data is sent in both directions …<br>– … but only one direction **at a time** | 4 |
| 8(c)(ii) | Any **two** from:<br>– Bits may arrive skewed<br>– More expensive **to setup/manufacture/purchase cable**<br>– Limited distance<br>– More prone to interference/error | 2 |

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